

cc

MIC-30 Cont.
(2002-0544.01)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee : Yangsung Joo
Assignee : Micron Technology, Inc.
Patent No. : 7,002,863 B2
Issued : February 21, 2006
Application No. : 10/783,976
Confirmation No. : 4392
Filed : February 20, 2004
For : DRIVING A DRAM SENSE AMPLIFIER HAVING LOW
THRESHOLD VOLTAGE PMOS TRANSISTORS
Art Unit : 2824
Examiner : Pho M. Luu

New York, New York 10020
October 31, 2006

ATTN: Certificate of Corrections
Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Certificate
NOV 07 2006
of Correction

REQUEST UNDER 37 C.F.R. §§ 1.322 AND 1.323
FOR CERTIFICATE OF CORRECTION

Sir:

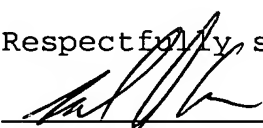
Enclosed is one (1) sheet of Form PTO/SB/44 (in duplicate) listing printing errors that have been found in the above-identified patent. The errors are clerical and self-explanatory and their correction does not constitute new matter and requires no reexamination.



At least one of the listed errors is believed to be the responsibility of the patentee. Please charge the \$100.00 payment of the fee set forth in 37 C.F.R. § 1.20(a) to Deposit Account No. 06-1075, Order No. 002322.0030. The Director of the U.S. Patent and Trademark Office is also hereby authorized to charge any additional fee due, or to credit any overpayment, in connection with this Request to Deposit Account No. 06-1075, Order No. 002322.0030. A duplicate copy of this Request is enclosed herewith.

Accordingly, patentee and assignee respectfully request that the U.S. Patent and Trademark Office issue a Certificate of Correction Under 37 C.F.R. §§ 1.322 and 1.323.

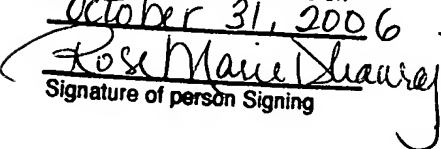
Respectfully submitted,



Michael J. Chasan
Reg. No. 54,026
Agent for Patentee and Assignee

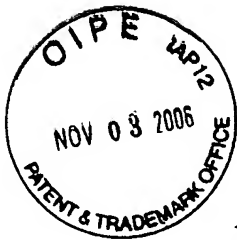
I hereby certify that this correspondence
is being deposited with the US Postal Service
as First Class Mail in an envelope Addressed to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450 on

October 31, 2006



Signature of person Signing

FISH & NEAVE IP GROUP
ROPES & GRAY LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
(212) 596-9000



MIC-30 Cont.
(2002-0544.01)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee : Yangsung Joo
Assignee : Micron Technology, Inc.
Patent No. : 7,002,863 B2
Issued : February 21, 2006
Application No. : 10/783,976
Confirmation No. : 4392
Filed : February 20, 2004
For : DRIVING A DRAM SENSE AMPLIFIER HAVING LOW
THRESHOLD VOLTAGE PMOS TRANSISTORS
Art Unit : 2824
Examiner : Pho M. Luu

New York, New York 10020
October 31, 2006

ATTN: Certificate of Corrections
Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REQUEST UNDER 37 C.F.R. §§ 1.322 AND 1.323
FOR CERTIFICATE OF CORRECTION

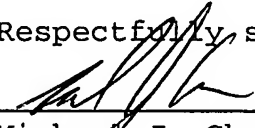
Sir:

Enclosed is one (1) sheet of Form PTO/SB/44 (in duplicate) listing printing errors that have been found in the above-identified patent. The errors are clerical and self-explanatory and their correction does not constitute new matter and requires no reexamination.

At least one of the listed errors is believed to be the responsibility of the patentee. Please charge the \$100.00 payment of the fee set forth in 37 C.F.R. § 1.20(a) to Deposit Account No. 06-1075, Order No. 002322.0030. The Director of the U.S. Patent and Trademark Office is also hereby authorized to charge any additional fee due, or to credit any overpayment, in connection with this Request to Deposit Account No. 06-1075, Order No. 002322.0030. A duplicate copy of this Request is enclosed herewith.

Accordingly, patentee and assignee respectfully request that the U.S. Patent and Trademark Office issue a Certificate of Correction Under 37 C.F.R. §§ 1.322 and 1.323.

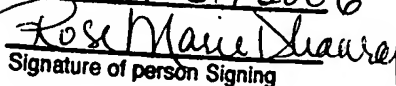
Respectfully submitted,



Michael J. Chasan
Reg No. 54,026
Agent for Patentee and Assignee

I hereby certify that this correspondence is being deposited with the US Postal Service as First Class Mail in an envelope Addressed to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450 on

October 31, 2006


Signature of person Signing

FISH & NEAVE IP GROUP
ROPES & GRAY LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
(212) 596-9000

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,002,863 B2

APPLICATION NO.: 10/783,976

ISSUE DATE: : February 21, 2006

INVENTOR(S) : Yangsung Joo

INVENTION : DRIVING A DRAM SENSE AMPLIFIER HAVING LOW THRESHOLD VOLTAGE PMOS TRANSISTORS

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 23, delete "is".

Column 4, line 3, change "11011" to --0--.

Column 7, line 27, change " Δ_1 " to -- ΔV_1 --.

Column 9, line 15, change " T_3 " to -- T_3 --.

Column 9, line 19, change "to digital 1"0,"" to --to digital "0,"--.

Column 9, line 30, change " ΔV_2 " to -- ΔV_2 --.

Column 9, line 46, change " ΔV_2 " to -- ΔV_2 --.

Column 9, line 60, change " ΔV_2 " to -- ΔV_2 --.

Column 10, line 1, change " ΔV_2 " to -- ΔV_2 --.

Column 13, line 1, delete "," after "for".

Column 14, line 17, change "FIG;" to --FIG.--.

Column 16, line 3, insert --be-- after "can".

Claim 6, column 19, line 34, change "claim 4" to --claim 5--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Michael J. Chasan
Reg. No. 54,026
Fish & Neave IP Group
Ropes & Gray LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020

Patent No. 7,002,863 B2

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

American LegalNet, Inc.
www.USCourtForms.com

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,002,863 B2

APPLICATION NO.: 10/783,976

ISSUE DATE: : February 21, 2006

INVENTOR(S) : Yangsung Joo

INVENTION : DRIVING A DRAM SENSE AMPLIFIER HAVING LOW THRESHOLD VOLTAGE PMOS TRANSISTORS

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 23, delete "is".

Column 4, line 3, change "11011" to --0--.

Column 7, line 27, change " Δ_1 " to -- ΔV_1 --.

Column 9, line 15, change " T_3 " to -- T_3 --.

Column 9, line 19, change "to digital 1"0,"" to --to digital "0,"--.

Column 9, line 30, change " ΔV_2 " to -- ΔV_2 --.

Column 9, line 46, change " ΔV_2 " to -- ΔV_2 --.

Column 9, line 60, change " ΔV_2 " to -- ΔV_2 --.

Column 10, line 1, change " ΔV_2 " to -- ΔV_2 --.

Column 13, line 1, delete "," after "for".

Column 14, line 17, change "FIG;" to --FIG.--.

Column 16, line 3, insert --be-- after "can".

Claim 6, column 19, line 34, change "claim 4" to --claim 5--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Michael J. Chasan
Reg. No. 54,026
Fish & Neave IP Group
Ropes & Gray LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020

Patent No. 7,002,863 B2

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

American LegalNet, Inc.
www.USCourtForms.com